

TSS40U

0.2Amp Surface Mount Schottky Barrier Diode

<u>0603</u>

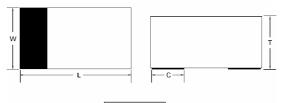


Features

- ♦ Designed for mounting on small surface
- ♦ Extremely thin/leadless package
- ♦ Low capacitance
- Low forward voltage drop
- High temperature soldering: 260°C/10 seconds at terminals
- ♦ Chip version in 0603

Mechanical Data

- ♦ Case: 0603 Standard package, molded plastic
- Terminals: Gold plated, solderable per MIL-STD-750, method 2026.
- Polarity: Indicated by cathode band
- ♦ Mounting position: Any
- ♦ Package code: RZ
- ♦ Weight: 0.003 gram (approximately)





ITEM	0603		
L	0.071(1.80)		
	0.063(1.60)		
w	0.039(1.00)		
	0.031(0.80)		
Т	0.033(0.85)		
	0.027(0.70)		
С	0.018(0.45)		
	Typical		
D	0.028(0.70)		
	Typical		

Dimensions in inches and (millimeters)

Maximum Ratings and Electrical characteristics

Rating at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%

Type Number	Symbol	0603	Units
Repetitive Peak Reverse Voltage	V _{RRM}	40	V
DC Reverse Voltage	V _R	40	V
RMS Reverse Voltage	V _{R(RMS)}	28	V
Average Forward Current	I _O	200	mA
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rate load (JEDEC method)	I _{FSM}	600	mA
Power Dissipation	Pd	150	mW
Forward Voltage IF=1.0mA IF=40mA	V _F	0.38 1.0	V
Reverse Leakage Current VR=30V	I _R	0.2	uA
Typical capacitance between terminals VR=0V, f =1.0MHz reverse voltage	CJ	5	pF
Reverse Recovery Time (IF=IR=10mA, Irr=0.1 x IR, RL=100Ω)	Trr	5	nS
Junction Temperature	TJ	-65 to + 125	°C
Storage Temperature	T _{STG}	-65 to + 125	°C



RATINGS AND CHARACTERISTIC CURVES(TSS40U)

Fig. 1 - Forward characteristics

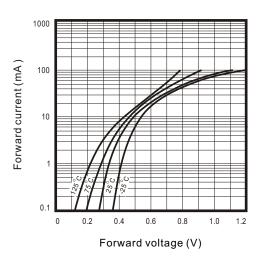


Fig.3 - Capacitance between terminals characteristics

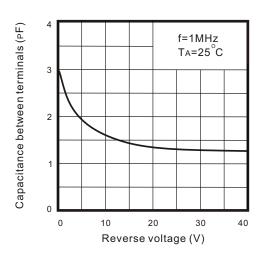


Fig. 2 - Reverse characteristics

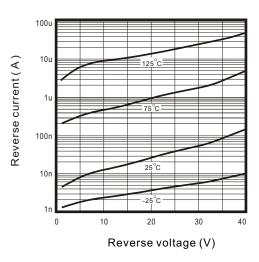


Fig.4 - Current derating curve

